



PATENT
1248-0538P
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IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:	Eiji YANAGAWA et al.	Conf.:	4277
Appl. No.:	09/820,671	Group:	2815
Filed:	March 30, 2001	Examiner:	S. Clark
For:	SEMICONDUCTOR DEVICE & METHOD OF MANUFACTURING THE SAME		

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

August 29, 2002

Sir:

Transmitted herewith is a Supplemental Amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	21	-	21	=	0	\$18	\$0.00
INDEPENDENT	5	-	5	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Charles Gorenstein, #29,271

GB
CG/CTB/mpe
1248-0538P

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ATTACHMENT

(Rev. 09/27/01)



PATENT
1248-0538P

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Amend
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eiji YANAGAWA et al. Conf. No.: 4277
Appl. No.: 09/820,671 Group: 2815
Filed: March 30, 2001 Examiner: S. Clark
For: SEMICONDUCTOR DEVICE & METHOD OF
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SUPPLEMENTAL AMENDMENT

Honorable Commissioner for Patents
Washington, D.C. 20231

August 29, 2002

Sir:

Further to our Response filed July 1, 2002 and in response to the Office Action dated April 1, 2002, the following supplemental amendments and amended remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please cancel claim 3 without prejudice or disclaimer thereto.

Please replace claims 1, 4, 7-9 and 22 with the following amended claims: Please note that claims 1, 4, 7-9 and 22 are presented below in their amended form. They are further presented as an Attachment to the Amendment whereby the amendments to the claims are outlined using the conventional method of bracketing and underlining.

1. (Twice Amended) A semiconductor device, in which a semiconductor element with an integrated circuit is secured to a board,

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